TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC74HC7240AP,TC74HC7240AF,TC74HC7244AP,TC74HC7244AF

Octal Bus Buffer (with schmitt trigger inputs)

TC74HC7240AP/AF Inverted, 3-State Outputs TC74HC7244AP/AF Non-Inverted, 3-State

Outputs

The TC74HC7240A/7244A are high speed CMOS OCTAL BUS BUFFERs with silicon gate  $C^2MOS$  technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

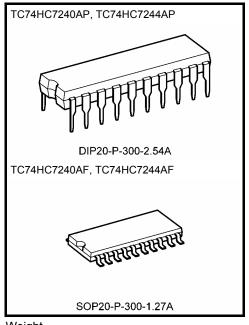
The TC74HC7240A/7244A have same pin configuration and function as the TC74HC240A/244A. And they have a hystereis characterictics with each input, so TC74HC7240A/7244A can be used as a line receiver, etc.

They have two active low output enables.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $t_{pd} = 15 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A \text{ (max)}$  at  $T_a = 25 \text{°C}$
- High noise immunity:  $V_H = 1.1 \text{ V (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: | I<sub>OH</sub> | = I<sub>OL</sub> = 6 mA (min)
- Balanced propagation delays:  $t_pLH \simeq t_pHL$
- Wide operating voltage range:  $V_{CC}$  (opr) = 2 to 6 V
- Pin and function compatible with 74LS240/244

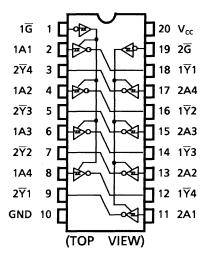


Weight

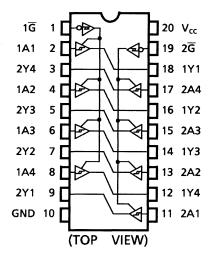
DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.)

### **Pin Assignment**

#### TC74HC7240A

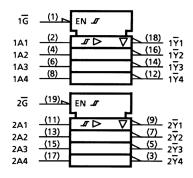


### TC74HC7244A



## **IEC Logic Symbol**

## TC74HC7240A



#### **TC74HC7244A**

1G	(1)	EN 🌃	1
1A1 1A2 1A3 1A4	(2) (4) (6) (8)	<b>I</b> ▷ ▽	(18) 1Y1 (16) 1Y2 (14) 1Y3 (12) 1Y4
2G	(19)	EN _IT	]
	(11)	-	

### **Truth Table**

Inp	uts	Outputs			
G	A <sub>n</sub>	Yn	$\overline{Y}_n$		
L	L	L	Н		
L	Н	Н	L		
Н	Х	Z	Z		

 $\Delta$ : For TC74HC7240A only

X: Don't care

Z: High impedance

## **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	$V_{CC}$	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	−0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±35	mA
DC V <sub>CC</sub> /ground current	Icc	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to  $65^{\circ}$ C. From Ta = 65 to  $85^{\circ}$ C a derating factor of -10 mW/°C shall be applied until 300 mW.

# **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	2 to 6	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	٧
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

## **Electrical Characteristics**

### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
				V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	J
				2.0	1.0	1.25	1.5	1.0	1.5	
Positive threshold voltage	$V_{P}$	_		4.5	2.3	2.7	3.15	2.3	3.15	V
remage				6.0	3.0	3.5	4.2	3.0	4.2	
				2.0	0.3	0.65	0.9	0.3	0.9	
Negative threshold voltage	$V_N$		_	4.5	1.13	1.6	2.0	1.13	2.0	V
				6.0	1.5	2.3	2.6	1.5	2.6	
				2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis voltage	$V_{H}$		_	4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
	V <sub>ОН</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	2.0	1.9	2.0	_	1.9	_	
				4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0	5.9	6.0	_	5.9	_	V
			$I_{OH} = -6 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -7.8 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
	V <sub>OL</sub> V <sub>IN</sub> = V <sub>I</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0		0.0	0.1	_	0.1	
			$I_{OL} = 20 \ \mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage				6.0		0.0	0.1	_	0.1	V
			I <sub>OL</sub> = 6 mA	4.5		0.17	0.26	_	0.33	
			$I_{OL} = 7.8 \text{ mA}$	6.0		0.18	0.26	_	0.33	
3-state output off-state current	I <sub>OZ</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or GND}$		6.0	_	_	±0.5	_	±5.0	μА
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or	GND	6.0	_	_	4.0	_	40.0	μА



AC Characteristics (input:  $t_r = t_f = 6$  ns)

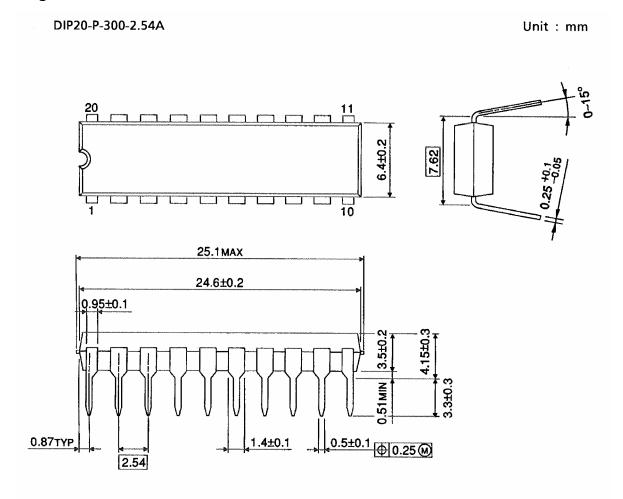
Characteristics	Symbol	Test Co	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	.,		CL (pF)	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
	t			2.0	_	25	60	_	75	
Output transition time	t <sub>TLH</sub>	_	50	4.5	_	7	12	_	15	ns
	t <sub>THL</sub>			6.0	_	6	10		13	
				2.0	_	50	125	_	155	
			50	4.5	_	15	25	_	31	
Propagation delay	$t_{pLH}$			6.0	—	13	21	_	26	ns
time	$t_{pHL}$			2.0	_	67	165	_	205	113
			150	4.5	_	20	33	_	41	
				6.0	—	17	28	_	35	
	t <sub>p</sub> zL t <sub>p</sub> zH	$R_L = 1 \text{ k}\Omega$		2.0	_	68	150	_	190	
			50	4.5	_	21	30	_	38	
Output enable time				6.0	_	16	26		32	ns
Output enable time			150	2.0	_	84	165	_	230	113
				4.5	_	26	37	_	46	
				6.0	_	20	31		39	
	4			2.0	_	48	150	_	190	
Output disable time	$R_L = 1 \text{ k}\Omega$	$R_L = 1 \text{ k}\Omega$	50	4.5	_	21	30	_	38	ns
	t <sub>pHZ</sub>			6.0	_	19	26		32	
Input capacitance	C <sub>IN</sub>	_	-		_	5	10		10	pF
Output capacitance	C <sub>OUT</sub>	_	-		_	10	_	_	_	pF
Power dissipation	C <sub>PD</sub>	TC74HC7240A			_	33	_	_	_	pF
capacitance	(Note)	TC74HC7244A			34	_		_	pΓ	

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$  (per bit)

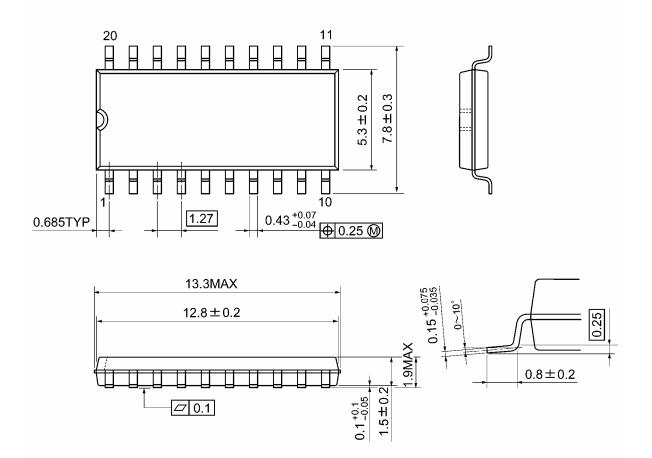
# **Package Dimensions**



Weight: 1.30 g (typ.)

# **Package Dimensions**

SOP20-P-300-1.27A Unit: mm



Weight: 0.22 g (typ.)

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20070701-EN GENERAL

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